



National Conference
on
Emerging Micro/Nano-Electronics:
Devices, Technology & VLSI Design (EMNE-2024)

7th & 8th November 2024

organised by

Department of Electronic Science
Kurukshetra University, Kurukshetra

in association with

Gurugram University, Gurugram



CERTIFICATE

This is to certify that **Rohan** of Dept of Engg & Techn, Gurugram University, Gurugram, Haryana, participated & presented a paper entitled “*Design and Circuit Simulation of Master-Slave D Flip-Flop Using Cadence Virtuoso: A Board Tutorial*”, in National Conference on **Emerging Micro/Nano-Electronics: Devices, Technology & VLSI Design**, organized by Department of Electronic Science, Kurukshetra University, Kurukshetra, on 7th and 8th November 2024.

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