

EMNE - 2024

# National Conference on

# Emerging Micro/Nano-Electronics: Devices, Technology & VLSI Design



Kurukshetra University,  
Kurukshetra, Haryana

in Collaboration with



Gurugram University  
गुरुग्राम विश्वविद्यालय

**Venue:- Electronic Science Department,  
Kurukshetra University, Kurukshetra, Haryana, India**  
**Dates :- 07-08 November, 2024**

**Conference website:- <http://emne.co.in>**

## ABOUT CONFERENCE

**EMNE-2024** will be organized by Electronic Science Department, Kurukshetra University, Kurukshetra, India on 07 to 08 November 2024, in collaboration with Gurugram University, Gurugram. This conference will provide a platform to academicians, researchers and technocrats to dwell deep into the most recent innovations, trends, concerns, and practical challenges encountered, as well as solutions adopted, in the fields of Micro/Nanoelectronics Devices, Semiconductor Fabrication Technologies, CMOS VLSI Circuits Design and CAD Tools. Their will be Distinguished Lectures, Keynote talks, Invited talks by leading industry leaders and renowned academicians. Original, unpublished research papers are invited for Oral & Poster presentations in the most relevant areas allied to the theme/tracks of the conference.

### MICRO/NANO DEVICES

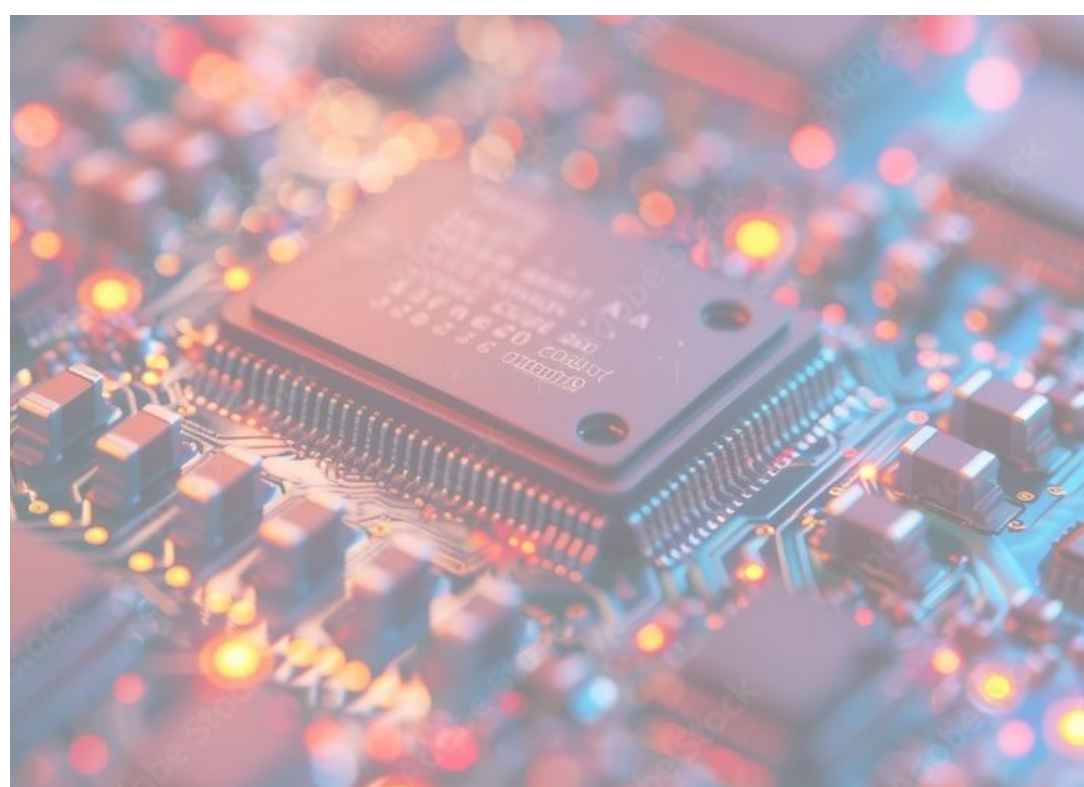
- Nano-CMOS Devices
- FinFET, GAAFET and Quantum devices
- Spintronics
- Emerging Devices and Materials Technology
- Sensors: Materials, Structures and Systems
- Renewable energy: Solar Cell Materials & Structures
- Emerging Memory Technologies
- CMOS Device Modelling
- MEMS Devices

### SEMICONDCUTOR FABRICATION TECHNOLOGY

- IC Integration
- Interconnects Technologies
- CMOS & Post CMOS Technologies
- Beyond 2D in packaging and interconnects
- Graphene and other 2D Materials
- Next generation Nanoscale Materials & Nanotechnology
- Advanced Processes & Materials

### VLSI DESIGN AND CAD TOOLS

- VLSI Circuits & System Design
- CAD for VLSI
- VLSI Testing & Verification
- IC Reliability
- Low power digital system
- Analog & Mixed Signals
- RF Microelectronics
- Digital CMOS Design



## IMPORTANT DATES

Last Date for Paper Submission	01 October 2024
Paper Acceptance Notification	15 October 2024
Last date for author Registration	01 November 2024
Date of Conference	07-08 November 2024

## REGISTRATION FEE

### On the Spot Registration

Industry	Rs. 4000/-
Faculty Memebtrs	Rs. 3000/-
Student	Rs. 1500/-

### Early Bird Registration (before 1 November 2024)

Industry	Rs. 3500/-
Faculty Memebtrs	Rs. 2500/-
Student	Rs. 1000/-

## PAPER SUBMISSION

Authors are invited to submit full-length original, unpublished papers along with an abstract of at most 200 words.

Papers will be accepted for oral and poster presentations at the conference. Every accepted paper must have at least one author registered for the conference.

*Paper Submission will be done through Microsoft CMT Paper submission system*

## CONFERENCE PROCEEDINGS

The proceeding of the conference will be published by the reputed publisher after peer reviewing of the full length papers.

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Vice-Chancellor, Kurukshetra University, Kurukshetra, Haryana

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**Prof. Dinesh Kumar**  
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